

In the Specification

Please amend the specification of this application as follows:

Rewrite the paragraph at page 6, line 25 to page 7, line 8 as follows:

-- Streams interface unit 207 sends data to the path that performs parallel interpretation of the composite postscript 208, printer control language PCL 210 or other PDL interpreter 210. Page pipeline block 209 re-assembles the results of the interpretation process into page format for page oriented processing before submitting page data to rendering unit 212. Streams interface 207, postscript interpreter 209, page pipeline block 209, PCL interpreter 210 and rendering unit 210 are controlled by PDL print controller 211. Postscript interpreter 208 or PCL interpreter 210 may send banded format data directly to rendering unit 212. Rendering unit 212 also performs compression, decompression or screening as required. PDL print controller to print engine controller interface unit 225 supplies data and control information to ASIC special purpose processor 213 to drive paper path control 216, the control panel/display 214 and the video data output 215.--

Rewrite the paragraph at page 12, line 8 to page 13, line 2 as follows:

-- Process 500 begins with start block 501. Process 500 first sets the current page as the first page and the current sub-band as the first sub-band of the current page description language file to be printed (processing block 502). Process 500 parses the display list of the page description language file for the current page to determine which display list elements will render to the current sub-band (processing block 503). Detailed rendering is not required. It is better to falsely determine that a display list

element will write to the sub-band than to improperly omit a display list element from this parsed list. Process 500 then transfers the parsed list of display elements for the current page and sub-band to SRAM 435 (processing block 504). Additional auxiliary data required to render these display element may also be transferred to the on-chip SRAM. This auxiliary data could be fonts, color tables and the like. These steps (processing blocks 502 and 503) store in data needed for rendering in SRAM 435. This serves to increase the rendering speed because of the high memory bandwidth between central processing unit 410 and SRAM 435. These steps are not necessary to obtain substantial benefit from this invention. Since the display list elements and the auxiliary data probably is less than the rendered sub-band data, the benefit of processing blocks 502 and 503 will not be as great as the benefit from the rendered data.--